

Software installation

Download the software from <u>www.rls.si/UPRGAM4096</u> by clicking the Documentation tab and selecting the file UPRGAM4096_Software. Once downloaded open the Setup.exe file and follow the installation wizard to complete installation.

NOTE: You should install the UPRGAM4096 software BEFORE connecting the UPRGAM4096 interface.

Note: Administrator rights are required to install the software.

Compatible operating systems: Windows XP, Windows Vista, Windows 7, Windows 10, Windows 11

Connecting the UPRGAM4096 interface and RMK4

After software installation connect the UPRGAM4096 to the computer using the USB cable.



Connect the RMK4 as shown.



JPRGAM4096 user manual

Starting the AM4096 interface program

Start the program by clicking the Desktop icon which has been created during installation or by selecting the program from the Start menu.

Once the application is started, you will see the following screen. Click the 'Power on' button.

| 🛎 AM4096 interface | | - 🗆 X |
|--|---|--|
| 1 | | |
| Show Display | | |
| Data Refresh Refresh 🗹 Continuously | Chip address (ADDR) 0 Set new address | Outputs direction (Sign) © CW © CCW |
| Relative position | Zero position (Zin) 0 Set zero position Zero | Vout/Tout pin function (Dact) Linear voltage Tacho |
| Counts Degrees | Resolution (Res) 0 4096 1024 256 64 2048 512 128 32 | Linear voltage period (Dac) © 360° 180° 90° 45° Tacha maggining space (Ctb) [1] |
| Absolute position | Digital hysteresis (Hyst) 0 (0.00°) V Set hysteresis | ○ 2048 512 128 32 ○ 1024 256 64 16 |
| Tacho | Regulator voltage (Reg35) ● 3.3 V ○ 3 V | SSI mode (SSIcfg) Single data, no ring register |
| Magnet status | Autonomous power down mode (Pdee) Disabled Enabled | Ring register, position not refreshed Ring register, position refreshed |
| AGC level | Power down duty ratio (Pdtr) [active : inactive] 1:128 1:256 1:512 1:1024 | Automatic gain control (AGCdis) © Enabled Disabled |
| Power ON Power OFF | Output function (BUFSEL) OUVW, tacho dir Osinusoidal diff. | Interpolator power (Pdint) Enabled Disabled |
| SAVE COAD | Number of pole pairs (UVW) ①1 ①2 ③3 ④4 ⑤5 ⑥6 ○7 ⑧8 | Incremental outputs (Abridis) © Enabled O Disabled |
| Interface not connected | | Software version: 2.1.0.3 |

The interface will now read the current programming / settings of the RMK4 and the screen should look something like this depending on the settings previously loaded.

| 🕉 AM4096 interface | | – 🗆 X |
|--------------------------------------|--|--|
| | | |
| Show Display | | |
| Data Refresh Refresh Continuously | Chip address (ADDR) | Outputs direction (Sign) © CW O CCW |
| Relative position | Zero position (Zin) 0 Set zero position Zero | Vout/Tout pin function (Dact) Linear voltage O Tacho |
| 1313 | Resolution (Res) ● 4096 ○ 1024 ○ 256 ○ 64 | Linear voltage period (Dac) ● 360° ○ 180° ○ 90° ○ 45° |
| Counts O Degrees | ○2048 ○512 ○128 ○32 | Tacho measuring range (Sth) [Hz] |
| Absolute position 1314 | Digital hysteresis (Hyst) 0 (0.00°) V Set hysteresis | ● 2048 ○ 512 ○ 128 ○ 32 ○ 1024 ○ 256 ○ 64 ○ 16 |
| Tacho 0.00 Hz / 0.0 RPM | Regulator voltage (Reg35) ● 3.3 V ○ 3 V | SSI mode (SSIcfg) Single data, no ring register |
| Magnet status Magnet in range | Autonomous power down mode (Pdee) Disabled Enabled | Ring register, position not refreshed Ring register, position refreshed |
| AGC level 18.8% | Power down duty ratio (Pdtr) [active : inactive] 1:128 1:256 1:512 1:1024 | Automatic gain control (AGCdis) Enabled Disabled |
| Power ON Power OFF | Output function (BUFSEL) OUVW, tacho dir O Sinusoidal diff. | Interpolator power (Pdint) Enabled Disabled |
| PROG READ SAVE Ioad | Number of pole pairs (UVW) ●1 ○2 ○3 ○4 ○5 ○6 ○7 ○8 | Incremental outputs (Abridis) Enabled O Disabled |
| Interface connected Chip is po | wered | Software version: 2.1.0.3 |

During 'Power on' the interface reads the data stored inside the internal EEPROM of the AM4096 chip and the software interface displays the read information graphically.



Programming and reading

The AM4096 can be re-programmed by changing the parameters within the software. Once the desired parameters have been selected, the 'PROG' button has to be clicked in order to save the changes to the chip.



The 'READ' button reads the current programming of the AM4096 chip. (If, for example, the parameters on the screen have been changed, but not saved, and the current programming needs to be read again, this can be done by pressing the 'READ' button.)

Different settings can be stored to the computer by pressing the 'SAVE' button. Previously saved settings can be uploaded by pressing the 'LOAD' button.

Parameter description: FIRST COLUMN

| 👗 AM4096 interface | | - |
|--|--|--|
| 1 | | |
| Show Display | | |
| Data Refresh Refresh 🗹 Continuously | Chip address (ADDR) 0 Set new address | Outputs direction (Sign) CW CCW |
| Relative position | Zero position (Zin) 0 Set zero position Zero | Vout/Tout pin function (Dact) Linear voltage OTacho |
| 706 | Resolution (Res) ● 4096 ○ 1024 ○ 256 ○ 64 | Linear voltage period (Dac) 360° 180° 90° 45° |
| Counts O Degrees | ○2048 ○512 ○128 ○32 | Tacho measuring range (Sth) [Hz] |
| Absolute position 706 | Digital hysteresis (Hyst) 0 (0.00°) V Set hysteresis | ● 2048 512 128 32 ○ 1024 256 64 16 |
| - Tacho 0.00 Hz / 0.0 RPM | Regulator voltage (Reg35) 3.3 V O 3 V | SSI mode (SSIcfg) Single data, no ring register |
| Magnet status Magnet in range | Autonomous power down mode (Pdee) Disabled Enabled | Ring register, position not refreshed Ring register, position refreshed |
| AGC level 18.8% | Power down duty ratio (Pdtr) [active : inactive] 1:128 1:256 1:512 1:1024 | Automatic gain control (AGCdis) Enabled Disabled |
| Power ON Power OFF | Output function (BUFSEL) UVW, tacho dir O Sinusoidal diff. | Interpolator power (Pdint) Enabled |
| V PROG V READ | Number of pole pairs (UVW) | Incremental outputs (Abridis) Enabled Disabled |
| | | 0.0 |

Refreshing data

The data from sensor can be refreshed continuously or manually. If data is refreshed continuously, the displayed position and other encoder data will constantly be refreshed whilst by **disabling the continuously refreshed** option the encoder data will only be refreshed once each time the user clicks the 'Refresh' button.

By default, the continuously refreshed option is enabled.

Position display

The software displays the position as relative and absolute. By default, the Relative position displays the position as a number (e.g. 686/4096, where the first number represents the relative position and the second number represents the full number of counts, or the resolution the chip is set to). This can be changed to 'Degrees' and the display now shows the relative position in degrees. *The Absolute position* always shows the position as an absolute number.

The position can also be displayed as a graphic form by ticking the 'Graphic representation' option.

Tacho output

When the Tacho output is used, the Tacho section displays the speed in Hertz and RPM (rotations per minute).

Magnet status

The 'Magnet status' section indicates whether the magnet is in range too close or too far. This helps control the distance between the magnet and the AM4096 chip.



| Magnet status | |
|-----------------|--|
| Magnet in range | |

Magnet too close

Magnet status

0.00 Hz / 0.0 RPM

Data Refresh Refresh 🗹 Continuously

Data Refresh

Refresh Continuously

A **RENISHAW** associate company

Magnet too far

User manual UPRGAM4096D01_03

Automatic gain control (AGC gain)

The AGC gain section displays the percentage of the automatic gain control. When AGC is disabled the AGC gain is fixed at 50.0%. If the AGC is enabled, then the AGC gain varies, depending on the magnet to chip distance. See AGC section on how to enable or disable.

AGC level 50.0%

Parameter description: SECOND COLUMN

| 👗 AM4096 interface | | × |
|----------------------------|--|---|
| | | |
| Show Display | | |
| Data Refresh | Chip address (ADDR) | Outputs direction (Sign) |
| Refresh Continuously | 0 Set new address | ● cw O ccw |
| Relative position | Zero position (Zin) | Vout/Tout pin function (Dact) |
| | 0 Set zero position Zero | ● Linear voltage ○ Tacho |
| 100 28° | Resolution (Res) | Linear voltage period (Dac) |
| | ● 4096 ○ 1024 ○ 256 ○ 64 | |
| O Counts | ○2048 ○512 ○128 ○32 | Tacho measuring range (Sth) [Hz] |
| Absolute position | Digital hysteresis (Hyst) | |
| 1141 | 0 (0.00°) V Set hysteresis | 01024 0256 064 016 |
| Tacho | Regulator voltage (Reg35) | SSI mode (SSIcfg) |
| 348.00 Hz / 20880.0 RPM | ● 3.3 V ○ 3 V | Single data, no ring register |
| Magnet status | Autonomous power down mode (Pdee) | O Ring register, position not refreshed |
| Magnet in range | O Disabled | O Ring register, position refreshed |
| AGC level | Power down duty ratio (Pdtr) [active : inactive] | Automatic gain control (AGCdis) |
| 93.8% | ● 1:128 ○ 1:256 ○ 1:512 ○ 1:1024 | Enabled Olisabled |
| Power ON Power OFF | Output function (BUFSEL) | Interpolator power (Pdint) |
| 💽 PROG 🛛 🕢 READ | Uvvv, tacho dir O Sinusoidai diff. | |
| SAVE 💕 LOAD | | Incremental outputs (Abridis) Opisabled |
| | | |
| erface connected Chip is p | owered | Software version: 2.1.0.3 |

Chip address

In the Chip address section the address of the device can be set from 0 to 127. This is used when more devices are interconnected, where each device needs to have an individual address. Type a number from 0 to 127 into the text box and confirm by pressing the 'Set the new address' button.

See page 6 of the AM4096D01 datasheet for a detailed description of this functionality.

NOTE – AM4096 chips with serial numbers AA3509 and AB3509 do not allow connecting more than one slave device to the TWI bus. This functionality is being added to the next revision of the product.

The RMK4 default setting is '0'.

Zero position

Zero position section user can set the relative zero position of the chip. The number represents the absolute position at which the relative zero position is (to be) set. There are two options to set the zero position of the chip:

By pressing the 'Set zero position' button virtual zero position will be set to the absolute zero position value inserted to the edit box. The value must be between 0 and resolution -1 otherwise an Error message will appear. E.g. with the resolution of 4096, the zero position value of 2048 will set the virtual zero position 180° to the absolute zero (note that the encoder position is displayed to the virtual zero position).

By pressing the 'Zero' button, current absolute position will be written to the zero position edit box and set the relative zero position according to the value. Therefore the displayed encoder position will be set to zero.

Any manual change of zero position value in the edit box has to be confirmed by clicking the 'Set zero position' button otherwise no change will be triggered (This is not the case for the change triggered by the 'Zero' button).

| Chip address (ADDR) | |
|---------------------|-----------------|
| 0 | Set new address |

Set zero position Zero

Zero position (Zin)

0

| selio | zei | 0. |
|-------|-----|----------|
| irmed | by | clicking |

Resolution

Here can be set resolution by selecting the required radio button. Resolution can be set to binary resolutions from 32 cpr (5 bit) to 4096 cpr (12 bit). It is in CPR (counts per revolution) or PPR (positions per revolution).

The RMK4 default setting is '4096' (12 bit).

Digital hysteresis (Hyst)

Setting the digital hysteresis enables the user to prevent backcounting although also contributes to the overall measuring inaccuracy. Certain hysteresis value means that when switching the rotation direction encoder position will be freezed until the hysteresis angle is reached.

To set the hysteresis the user has to select the desired value (angle) from the drop box and click the 'set hysteresis' button.

Regulator voltage

You can set regulator voltage if a power supply of 5 V is used, the 3.3 V option should be selected or if a power supply of 3 V is used, the 3 V option should be selected.

The RMK4 default setting is '3.3 V' (for 5 V power supply).

Autonomous power down mode and Power down duty ratio (active : inactive)

In this part the power save mode can be enabled or disabled by selecting the appropriate button.

If enabled, the power down duty ratio can be selected. This means that the position is not read continuously, but in intervals as selected.

The RMK4 default setting is 'Disabled'.

Bufsel and UVW - number of pole pairs

The output on pins 5, 6, 7 and 8 can be selected between UVW/Tacho and differential sinusoidal. If the UVW outputs are selected, the number of pole pairs of the UVW commutation signals can be selected.

The RMK4 default setting is 'UVW, tacho dir' and the default number of pole pairs is 1.

| Resolutio | n (Res) | | | |
|-----------|---------|-------|------|--|
| 4096 | O 1024 | 0 256 | 064 | |
| 0 2048 | 0512 | O 128 | O 32 | |

| Digital hysteresis (Hyst) | |
|---------------------------|----------------|
| 0 (0.00°) v | Set hysteresis |

| Regulator | voltage (Reg35) | |
|-----------|-----------------|--|
| 0 3.3 V | ○ 3 V | |

6

| Autonomous power | down mode (Paee) | |
|------------------|--------------------|--|
| Disabled | \bigcirc Enabled | |

| Autonomous power down mode (Pdee) | | | | |
|--|--------|--------|---------|--|
| O Disabled Enabled | | | | |
| Power down duty ratio (Pdtr) [active : inactive] | | | | |
| 1:128 | 01:256 | 01:512 | O1:1024 | |

Output function (BUFSEL) UVW, tacho dir ○ Sinusoidal diff. Number of pole pairs (UVW) ●1 ○2 ○3 ○4 ○5 ○6 ○7 ○8





Parameter description: THIRD COLUMN

| | | > |
|--|--|--|
| | | ARLS |
| | | |
| Chip address (ADDR) Output | its direction (| Sign) |
| 0 Set new address | | ○ ccw |
| Zero position (Zin) Vout/ | Tout pin fund | tion (Dact) |
| 0 Set zero position Zero O Line | ar voltage | Tacho |
| Resolution (Res) | voltage perio | od (Dac) |
| ● 4096 ○ 1024 ○ 256 ○ 64 ◎ 360 | ° 0 180° | ○ 90° ○ 45° |
| ○2048 ○512 ○128 ○32 Tacho | measuring r | ange (Sth) [Hz] |
| Digital hysteresis (Hyst) | 8 0 512 | ○128 ○32 |
| 0 (0.00°) V Set hysteresis 0102 | 4 0256 | ○64 ○16 |
| Regulator voltage (Reg35) SSI m | node (SSIcfg) | |
| ● 3.3 V ○ 3 V ● Sing | gle data, no r | ing register |
| Autonomous power down mode (Pdee) | g register, po | sition not refreshed |
| O Disabled O Ring | g register, po | sition refreshed |
| Power down duty ratio (Pdtr) [active : inactive] Autom | atic gain cont | trol (AGCdis) |
| ● 1:128 ○ 1:256 ○ 1:512 ○ 1:1024 ● Ena | bled | ◯ Disabled |
| Output function (BUFSEL) Interp | olator power | (Pdint) |
| ● UVW, tacho dir ○ Sinusoidal diff. | bled | ○ Disabled |
| Number of pole pairs (UVW) | nental output | s (Abridis) |
| ●1 ○2 ○3 ○4 ○5 ○6 ○7 ○8 ●Ena | bled | O Disabled |
| | Chip address (ADDR) Output 0 Set new address 2ero position (Zin) Output 0 Set zero position Zero 4096 1024 256 64 360 2048 512 128 32 Tache Digital hysteresis (Hyst) 0 0 Set sero position Cin 0 (0.00°) Set hysteresis 1002 Set sero position Cin Set new address 0 (0.00°) Set hysteresis 0102 Set new address Sin Sin 0 (0.00°) Set hysteresis 0102 Set new address Sin Sin 0 (0.00°) Set hysteresis 0102 Sin Sin Sin 0 sinsolad (Fdt) Genebled Rin Power down duty ratio (Pdtr) [active : inactive] Autonomous power down for Osinusoidal diff. Interp 0 utput function (BUFSEL) Interp Uutput function (BUFSEL) Interp © 1 0 2 0 0 4 0 0 0 4 0 0 0 0 7 Set new Set new | Chip address (ADDR) Outputs direction (0 Set new address Zero position (Zin) Vout/Tout pin func 0 Set zero position Zero 0 Linear voltage Linear voltage peric 0 124 256 64 360° 180° Tacho measuring r Digital hysteresis (Hyst) 02048 512 0 0.00°) Set hysteresis 1024 256 Regulator voltage (Reg35) SSI mode (SSIdg) 033 Single data, no r 0 Disabled Tenabled Ring register, po Power down duty ratio (Pdtr) [active : inactive] Automatic gain conto Enabled 0utput function (BUFSEL) Interpolator power Enabled 0utput function ga is (UWW) Incremental output Enabled Number of pole pairs (UWW) Enabled Enabled |

Outputs direction

Output direction can be selected to be either CW (clockwise) or CCW (counter clockwise). The RMK4 default setting is 'CW'.

Vout/Tout pin function, DA data period and Tacho measuring range [Hz]

In this part the output on pin 20 can be selected between Linear voltage ('Vout') and Tacho ('Tacho').

If 'Vout' is selected, the period of the linear voltage output can be selected. See page 14 of the AM4096D01 datasheet for a detailed description of the Linear voltage output.

If 'Tacho' is selected, the measuring range can be selected (in Hz). See page 13 of the AM4096D01 datasheet for a detailed description of the Tacho output.

The RMK4 default setting is 'Vout' and the default period is '360°'. The default setting for the Tacho measuring range [Hz] is '2048'.

SSI mode

Here you can select an operating mode of the SSI output. See page 11 of the AM4096D01 datasheet for a detailed description of this functionality.

The RMK4 default setting is 'Single data, no ring register'.

AGC

You can select that AGC (automatic gain control) can be enabled or disabled.

The RMK4 default setting is 'Enabled'.

Outputs direction (Sign) ● CW ○ CCW

Vout/Tout pin function (Dact) Linear voltage O Tacho Linear voltage period (Dac) 360° 0 180° 0 90° 0 45°

| Vout/Tout pin function (Dact) | | | | |
|-------------------------------|-------------|------------|--------------|--|
| ○ Linear voltage | | | | |
| Linear voltage period (Dac) | | | | |
| 360° | ○ 180° | ○ 90° | ○ 45° | |
| Tacho m | easuring ra | ange (Sth) | [Hz] | |
| 2048 | 0512 | O 128 | O 32 | |
| 0 1024 | O 256 | 064 | ◯ 16 | |

SSI mode (SSIcfg) Single data, no ring register Ring register, position not refreshed Ring register, position refreshed

Automatic gain control (AGCdis) Enabled
 O Disabled

Interpolator power

Here you can selected that the interpolator power can be enabled or disabled.

The RMK4 default setting is 'Enabled'.

Incremental outputs

In this section the incremental outputs can be enabled or disabled.

The RMK4 default setting is 'Enabled'.

Show display

For the purpose of presentations, the large analogue display panel was designed. The display can be shown by checking the 'show display' check box on the top of the main interface.

◯ Disabled Enabled 🛎 AM4096 interface SAVE DISPLAY SETTINGS LOAD DISPLAY SETTINGS Show Display Chip address (ADDR) Outputs direction (Sign) Refresh Continuously Occw 0 Set new address ● CW Relative position Zero position (Zin) Vout/Tout pin function (Dact) O Linear voltage 0 Set zero position Zero Tacho 293.47° Resolution (Res) Linear voltage period (Dac)

064

032

Set hysteresis

360° ◯ 180° 0 904

○1024 ○256 ○64

 Single data, no ring register O Ring register, position not refreshed

 \bigcirc Ring register, position refreshed

O Disabled

Olisabled

Olisabled

Automatic gain control (AGCdis)

Interpolator power (Pdint)

Incremental outputs (Abridis)

SSI mode (SSIcfg)

Enabled

Enabled

Enabled

0 450

0 32

016

| | Interface connected | Chip is powered | Software version: 2.1.0.3 |
|--|---------------------|----------------------------|--|
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| The analogue display enables the user to select one of the tw | o display mod | es: the angular pos | ition display (by selecting the 'Position' |
| The dialogue display enables the door to select one of the tw | | be discussed in the second | international static second states and the |
| radio button on the bottom) or the tachometer (the Tacho rad | lo button) whic | ch displays the rota | ing velocity in number of turns per |
| second (Hz). The position display also enables resetting the p | osition by click | king the 'RESET' bι | Itton which acts the same way as the |
| 'Zere' button on the main interface (ace 'Zere position' acetion | on the ton) | 9 | , , |
| Zero button on the main interface (see Zero position section | on the top). | | |

Data Refresh

0.00 Hz / 0.0 RPM

Magnet in range

Power OFF

💕 LOAD

Absolute position

Magnet status

Power ON

🔮 PROG

🛃 SAVE

AGC level 93.8%

Tacho

4096

0 2048

0 (0.00°)

3.3 V

○ Disabled

0 1024

0512

Digital hysteresis (Hyst)

Regulator voltage (Reg35)

Output function (BUFSEL)

Number of pole pairs (UVW)

UVW, tacho dir

Autonomous power down mode (Pdee)

Power down duty ratio (Pdtr) [active : inactive]

● 1:128 ○ 1:256 ○ 1:512 ○ 1:1024

●1 ○2 ○3 ○4 ○5 ○6 ○7 ○8

0 256

0128

03V

Enabled

O Sinusoidal diff.



Interpolator power (Pdint) Enabled ◯ Disabled

Incremental outputs (Abridis)

Another display feature is the orange arrow on the very top left hand corner of the main interface. Pressing the arrow will reduce the main interface by hiding the operating board, and moving the digital position display to the top of the interface. Reduced interface can be combined with the analogue display so that the analogue display does not cover the main interface on the screen.

The original full-sized interface with the operating board can be restored by pressing the orange arrow on the reduced board.

| 👗 AM4096 interface | | | | - | • × | |
|---------------------|-----------------|----------------|---------------------------|---|------|--|
| 1 | | 261.74° | 2978 /4096 | 9 | RLS' | |
| Show Display | | | | | | |
| Interface connected | Chip is powered | | Software version: 2.1.0.3 | | | |
| | | | | | | |

| 👗 AM4096 interface | | - 🗆 X |
|--|---|--|
| | | ARLS |
| Show Display | | |
| Data Refresh Refresh 🗹 Continuously | Chip address (ADDR) 0 Set new address | Outputs direction (Sign) CW CCW |
| Relative position | Zero position (Zin) 0 Set zero position Zero | Vout/Tout pin function (Dact) O Linear voltage Tacho |
| 149.33° | Resolution (Res) • 4096 0 1024 0 256 0 64 | Linear voltage period (Dac) ◎ 360° 180° 90° 45° |
| ○ Counts | ○ 2048 ○ 512 ○ 128 ○ 32 | Tacho measuring range (Sth) [Hz] |
| Absolute position 1699 | Digital hysteresis (Hyst) 0 (0.00°) V Set hysteresis | ● 2048 ○ 512 ○ 128 ○ 32 ○ 1024 ○ 256 ○ 64 ○ 16 |
| Tacho 0.00 Hz / 0.0 RPM | Regulator voltage (Reg35) () 3.3 V) 3 V | SSI mode (SSIcfg) Single data, no ring register |
| Magnet status Magnet in range | Autonomous power down mode (Pdee) O Disabled | Ring register, position not refreshed Ring register, position refreshed |
| AGC level 62.5% | Power down duty ratio (Pdtr) [active : inactive] • 1:128 | Automatic gain control (AGCdis) Enabled Disabled |
| Power ON Power OFF | Output function (BUFSEL) Output function (BUFSEL) Output function (BUFSEL) | Interpolator power (Pdint) Enabled Disabled |
| V PROG READ | Number of pole pairs (UVW) 1 2 3 4 5 6 7 8 | Incremental outputs (Abridis) Enabled Disabled |
| nterface connected Chip is p | owered | Software version: 2.1.0.3 |

NOTE: All of the changes made will not be programmed to the encoder until the PROG button has been clicked.



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Document issues

| Issue | Date | Page | Corrections made |
|-------|--------------|---------|---------------------------|
| 1 | 20. 11. 2009 | - | New document |
| 2 | 25. 5. 2018 | General | Software version: 2.1.0.3 |
| 3 | 18. 12. 2024 | 1 | Windows 11 added |

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